



DETERMINISTIC6G

6G Emulation for Exoskeleton Control

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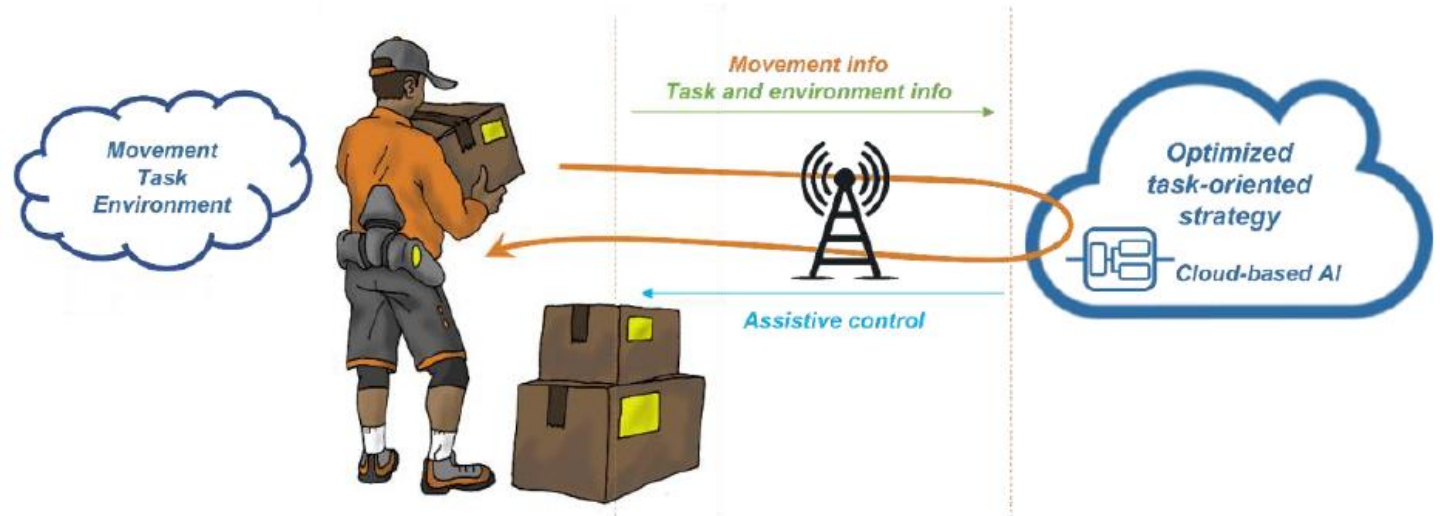


Sant'Anna
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«Cloud is the limit»: the target

- ❑ The target user of the use case is a worker of a logistic warehouse with a lumbar active occupational exoskeleton



- ❑ 6G network: Allows the user to log into the cloud-based system and to receive customized exoskeleton configurations
- ❑ Cloud AI-based assistant: collect information from different sensors and to determine task-oriented assistive strategies.

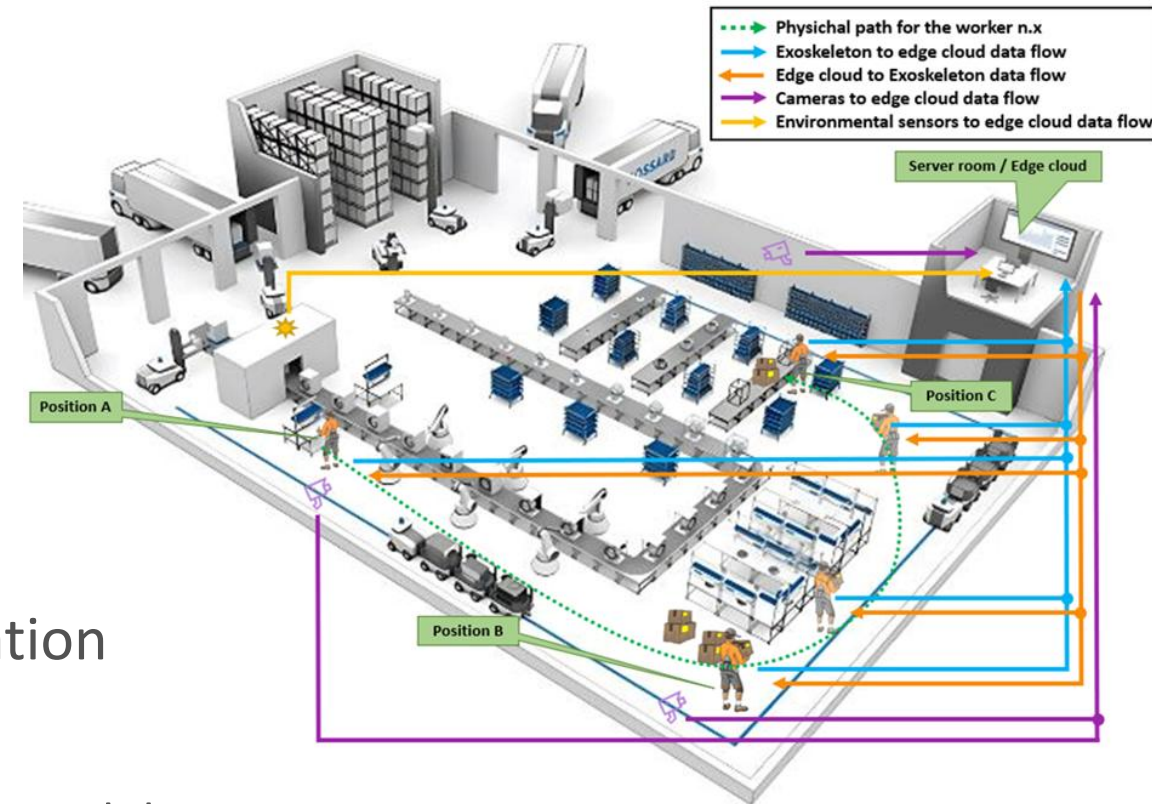
«Cloud is the limit»: the target

- ❑ In the cloud is possible to create a digital twin of the system to remotely monitor movements and provide real-time feedback and suggestions.
- ❑ This allows to have multiple exoskeletons and multiple digital twins sharing the perceived data acting independently.
- ❑ As well, it is possible to integrate data coming from other sensors (i.e. cameras) in the “Smart Factory”

Enabling technology: Offload computation

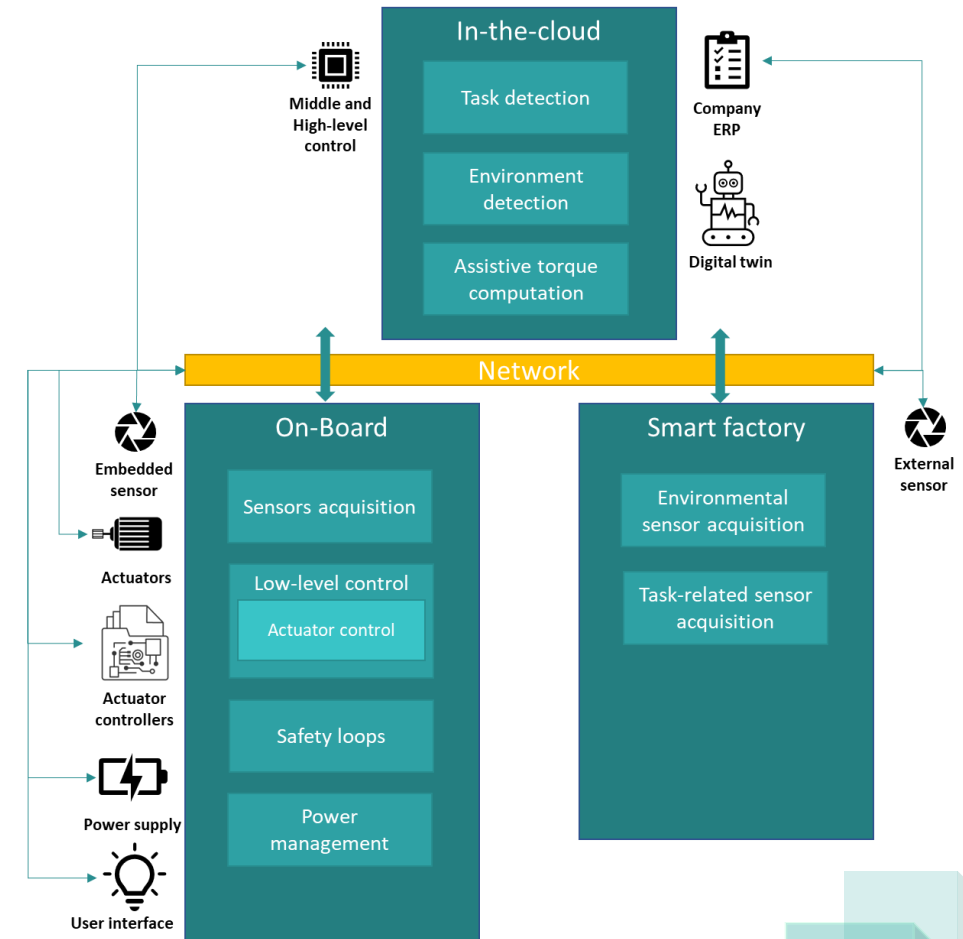


The low-level control is embedded in the exoskeleton, while the middle and high-level control are delocalized in the cloud.



Why Offload Computation?

- ❑ The exoskeleton includes an embedded processor that runs a low-level controller, which translates the desired joint assistance torque into current and voltage setpoints to drive the motors accordingly.
- ❑ The middle-level and high-level controllers are offloaded to the cloud relying on 6G-based wireless network, as well as the hardware components on-board the exoskeleton, thus reducing power consumption.
- ❑ A delocalized controller, monitoring, and real-time processing of a huge amount of information gathered from multiple subsystems will be made possible by enabling off-board complex AI assistive strategies.



Why Offload Computation?

Offloading computation offers several advantages:

- ☐ Reduced cost (one/few powerful machines in the cloud replace a lot of powerful hardware on many exoskeletons)
- ☐ Reduced size and weight (smaller batteries)
- ☐ Reduced power consumption
- ☐ Full integration with the smart factory digital ecosystem
- ☐ Adaptive support based on the user's or environment's inputs
- ☐ However, as the device architecture becomes less embedded and based on external computation, it must rely on highly dependable communication to ensure safety and consistency of operation



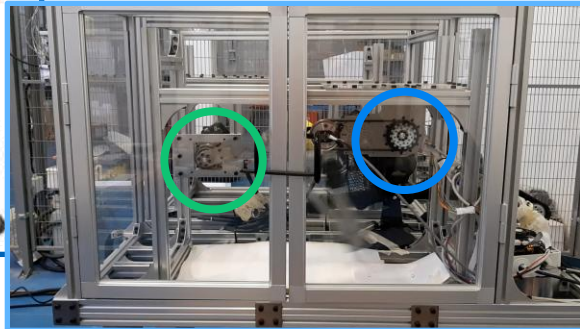
Understand the effect of offload computation
and network delay into exoskeleton control

Objective and overall architecture

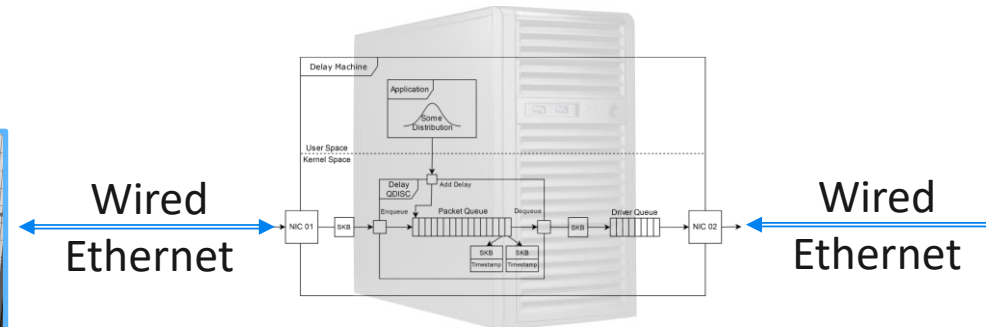
- ❑ Emulate the effect of a 6G network in an occupational exoskeleton



**Exoskeleton
Test Bench**



**Linux Machine
6G Network emulator**



Edge Cloud Server & App

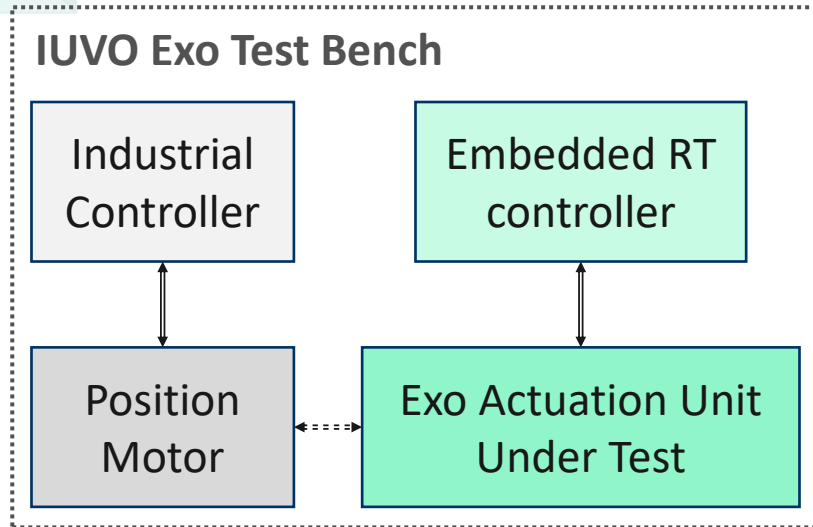


- ❑ IUVO test bench to evaluate reconfigurable and repetitive work cycle performances for exoskeleton actuation unit
- ❑ University of Stuttgart 6G Network emulator tool (more details tomorrow!)

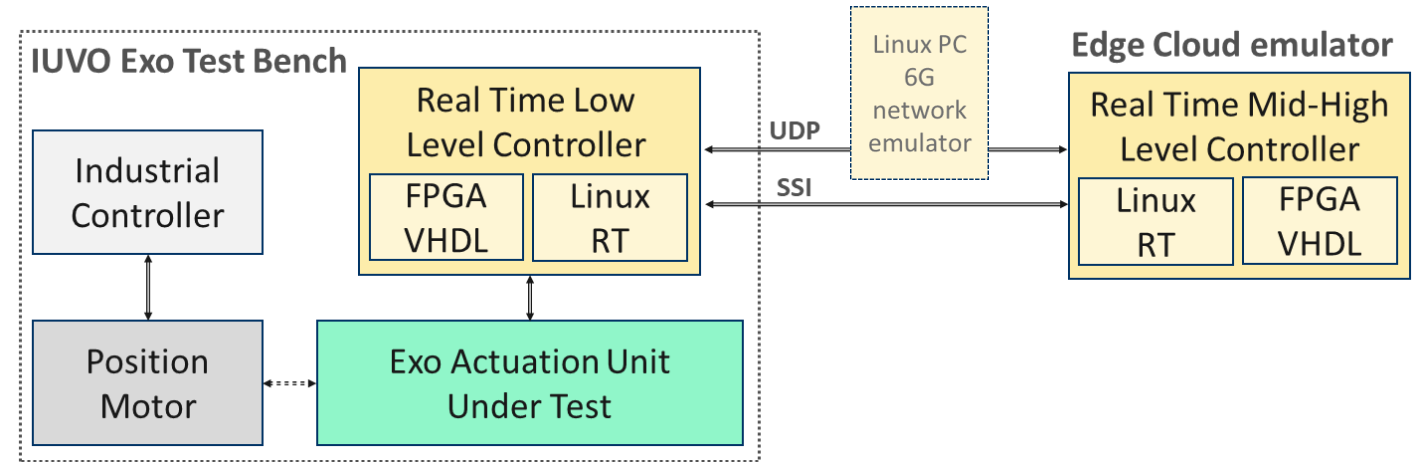
Emulator integration:

Exo Test Bench, 6G Network Emulator

Test Bench, basic configuration:

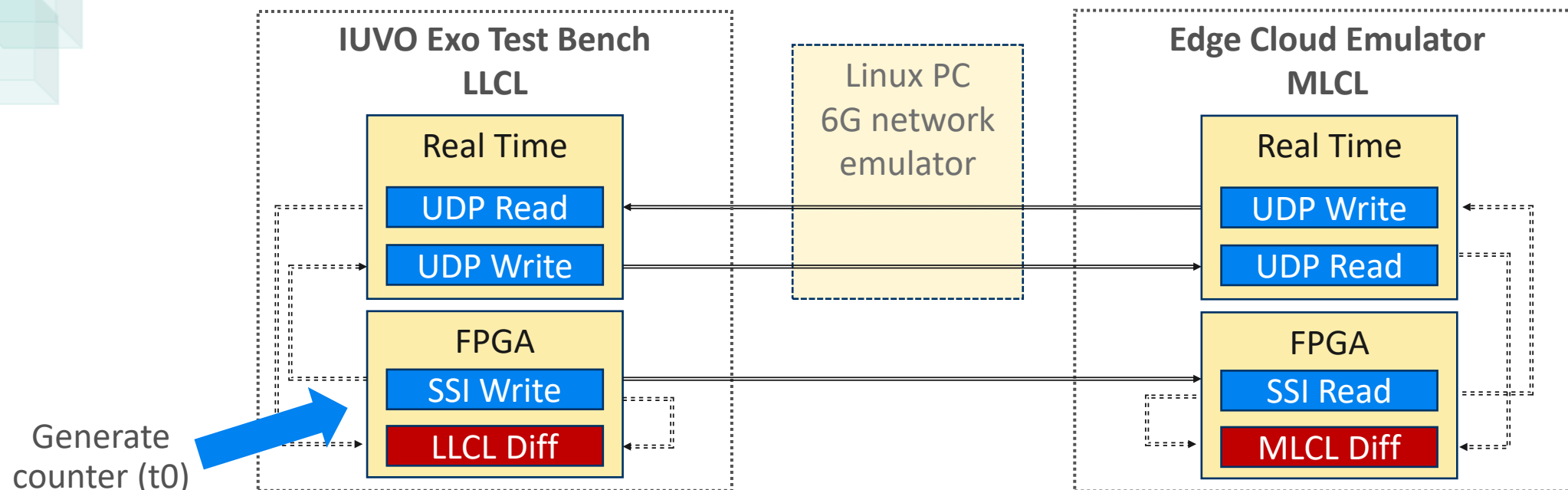


Updated Test Bench with DET6G emulator:



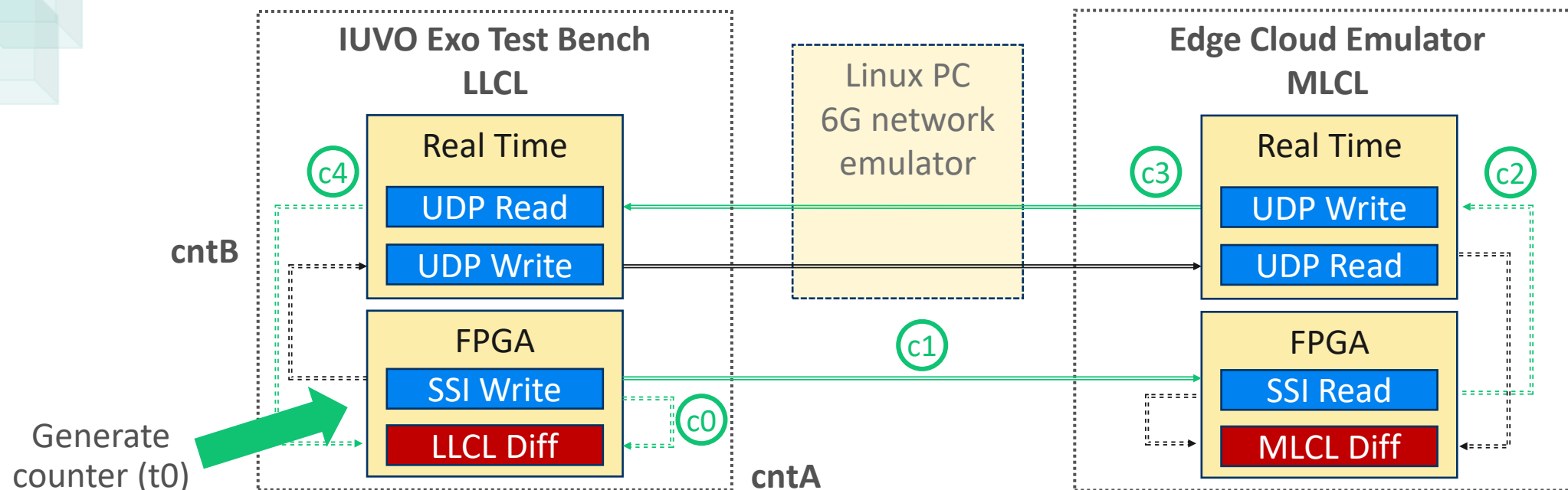
- ❑ UDP connection between the Exoskeleton Test Bench (ETB) and the “Edge Cloud Emulator” (ECE)
- ❑ Linux PC installed on the UDP line and it can be set as “transparent forwarder” or as 6G network emulator
- ❑ Synchronous Serial Interface parallel line works in real-time, and it synchronizes the ETB and ECB, and it permits diagnostic of the overall architecture

Network Architecture



- ❑ Investigate how and how much the network introduces misalignments between the low-level control (LLCL) and the high-middle-level control (MLCL)
- ❑ Idea: share a counter generated at the FPGA level of the LLCL through two different pathways to see the time needed to complete the roundtrip and highlight the network contribution

Network Architecture

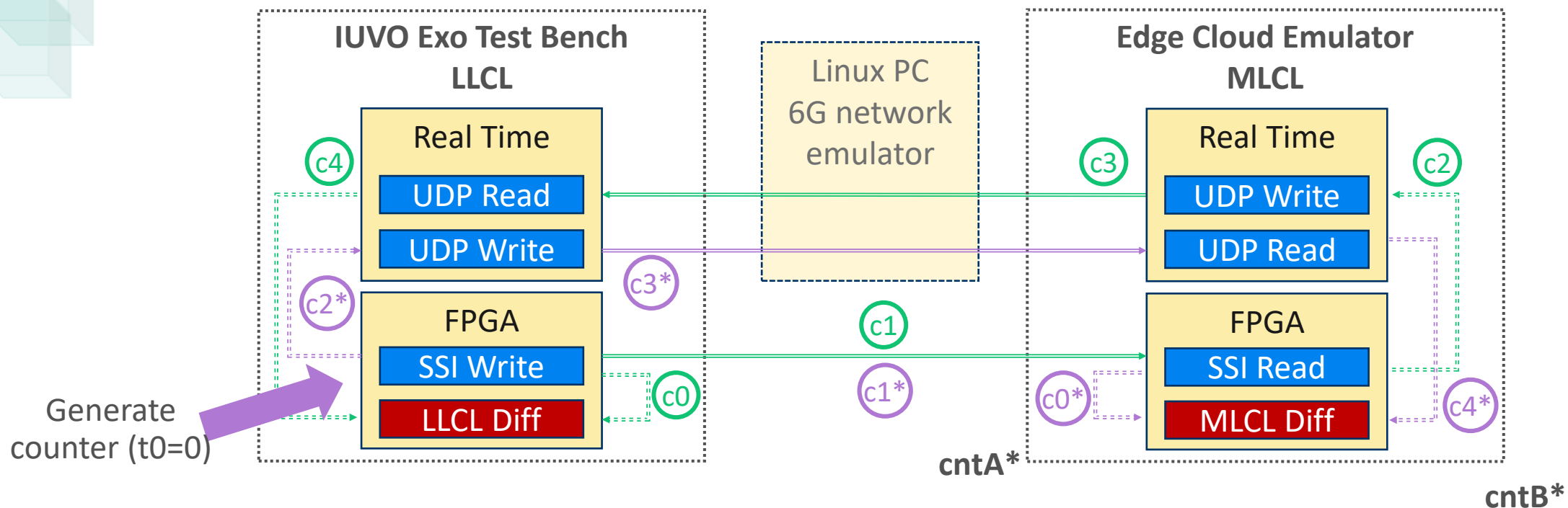


LLCL Routes

cntA => **c0**

cntB => **c1 + c2 + c3 + c4**

Network Architecture



MLCL Routes

$$\text{cntA}^* \Rightarrow c1^* + c0^*$$

$$\text{cntB}^* \Rightarrow c2^* + c3^* + c4^*$$

Network Architecture

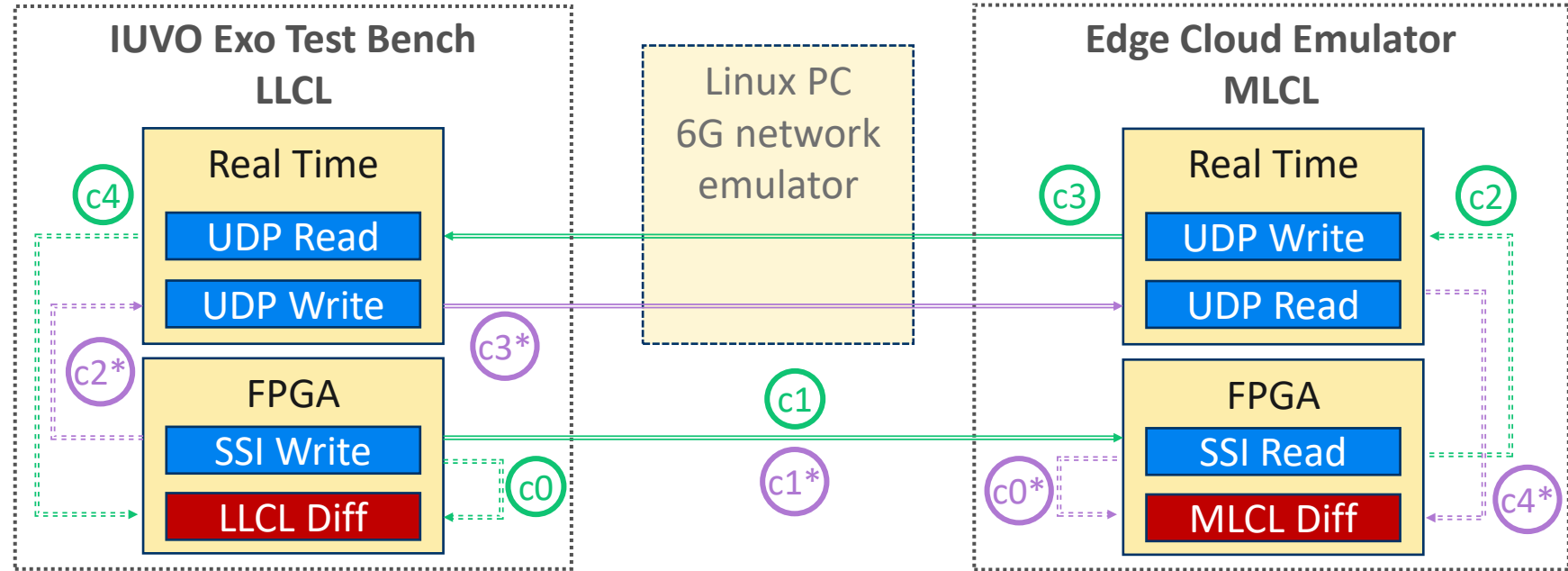
t₀: instant where the FPGA SSI counter begin send.
The variable «Emulated Counter [cnt]» in the LLCL is updated at t₀

The variable «Emulated Counter [cnt]» in the LLCL is updated at t₀

Scan Engine (c₂, c₄, c₂^{*}, c₄^{*}):

- User Defined Variable (UDV-UI16)
- Boolean Trigger

n: number of RT loops (at 100 Hz)
to wait to receive the counter



LLCL Diff = cntA – cntB = n (updated every 10 ms)

c₀: $C_{LV_{FPGA \rightarrow FPGA}} = 500 \text{ } \mu\text{s}$ because diff is at 2kHz

c₁: $C_{SSI_{LL \rightarrow ML}} = 18 * \frac{1}{5 \text{ kHz}} = 3,6 \text{ ms}$

c₂: $C_{SE_{FPGA \rightarrow RT}} = 500 \text{ } \mu\text{s} + 50 \text{ } \mu\text{s}$

c₃: $C_{UDP_{ML \rightarrow LL}} = ?$

c₄: $C_{SE_{RT \rightarrow FPGA}} = 500 \text{ } \mu\text{s}$

MLCL Diff = cntA^{*} - cntB^{*} = n^{*} (updated every 10 ms)

c₀^{*}: $C_{LV_{FPGA \rightarrow FPGA}} = 500 \text{ } \mu\text{s}$ because diff is at 2kHz

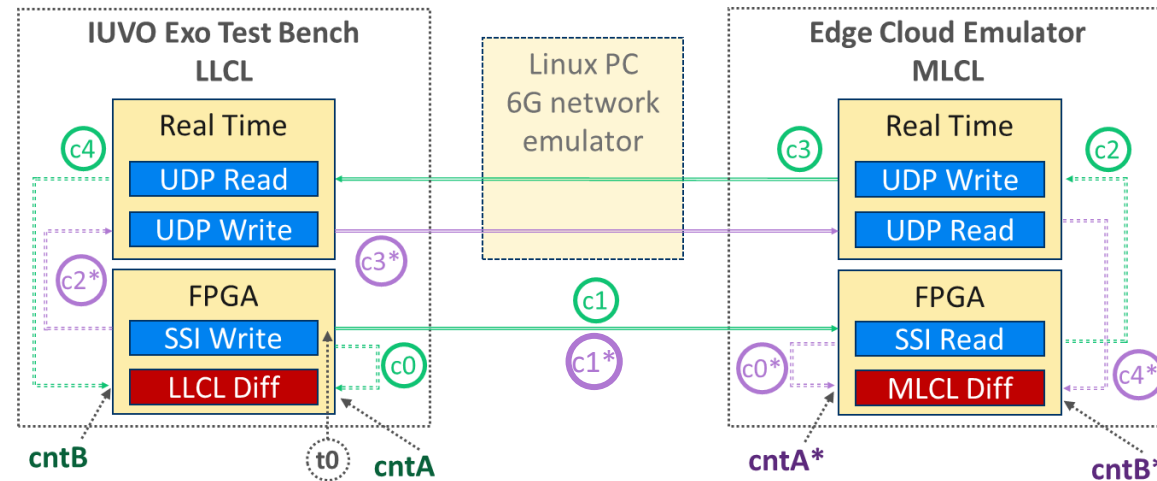
c₂^{*}: $C_{SE_{FPGA \rightarrow RT}} = 500 \text{ } \mu\text{s}$

c₃^{*}: $C_{UDP_{LL \rightarrow ML}} = ?$

c₄^{*}: $C_{SE_{RT \rightarrow FPGA}} = 500 \text{ } \mu\text{s}$

c₁^{*} = c₁

Emulator network architecture, delay study



$$\text{LLCL Diff} \rightarrow n * 10 \text{ ms} = (cnt_A - cnt_B) * 10 \text{ ms} = -c_0 + (c_1 + c_2 + c_3 + c_4) \\ (4,15 \text{ ms} + c_3) = n * 10 \text{ ms} \rightarrow c_3 = (n * 10 - 4,15) \text{ ms}$$

$$\text{MLCL Diff} \rightarrow n^* * 10 \text{ ms} = (cnt_{A^*} - cnt_{B^*}) * 10 \text{ ms} = -(c_0^* + c_1^*) + (c_2^* + c_3^* + c_4^*) \\ - 3,1 \text{ ms} + c_3^* = n^* * 10 \text{ ms} \rightarrow c_3^* = (n^* * 10 + 3,1) \text{ ms}$$

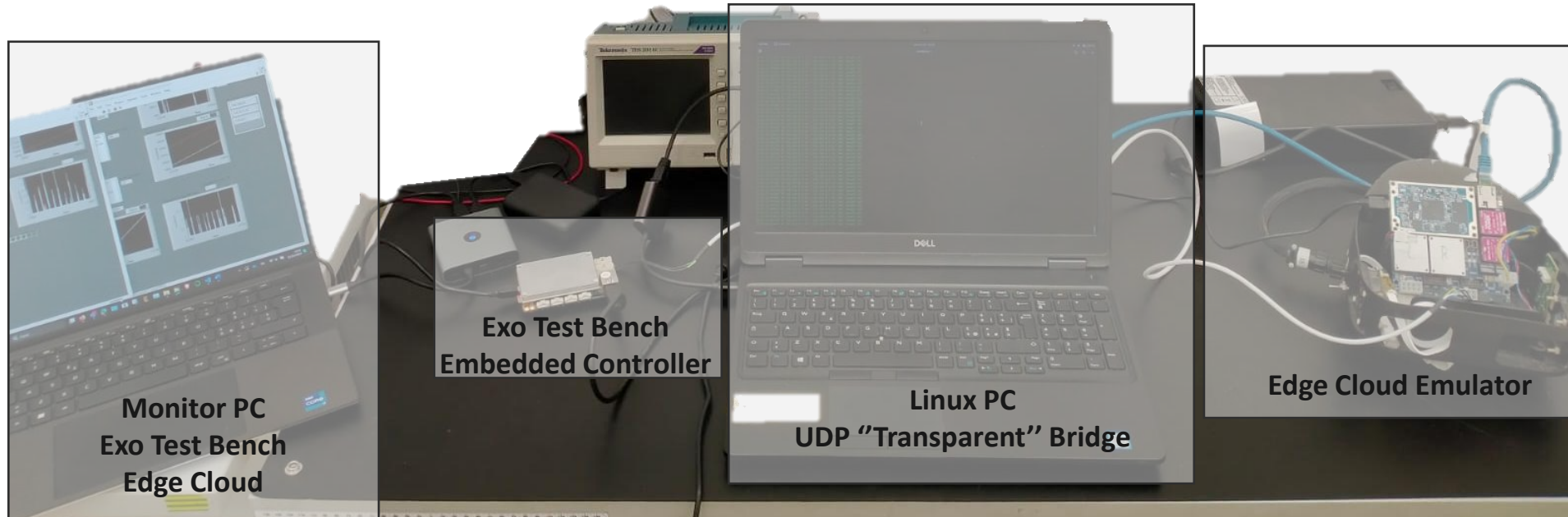
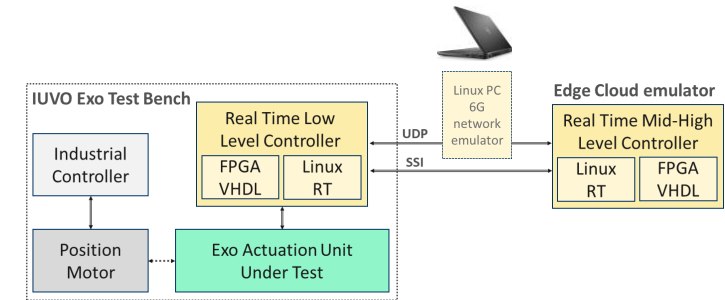
Notes:

The 6G Network Emulator breaks c_3 and c_3^* .

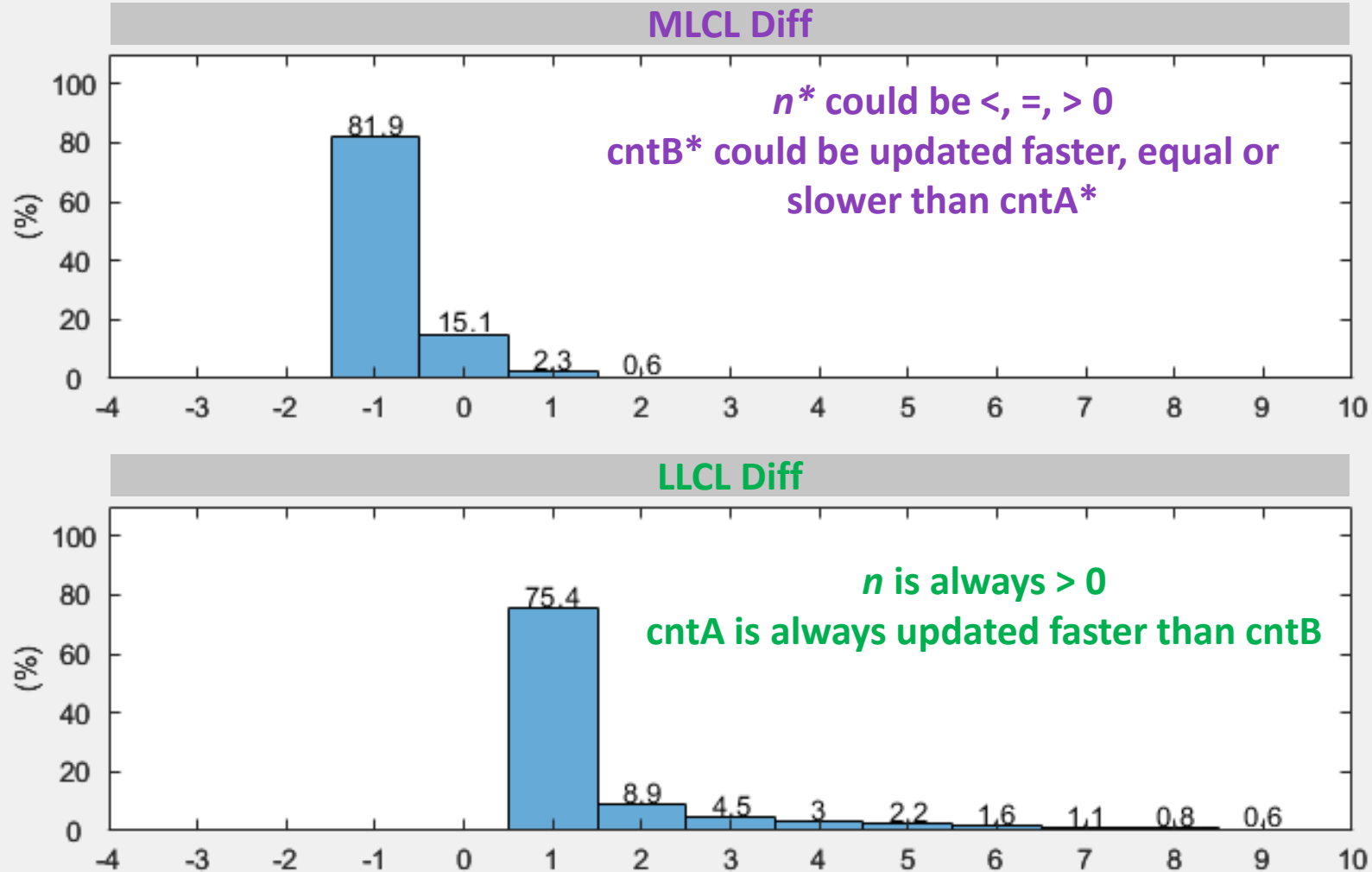
This introduces some additional delay, even if the emulator operates as transparent forwarder.

Debian PC transparent bridge verification

- Performed 30min acquisition test with the emulated counter differentiation performed both in RT or FPGA



Example of result, 30min acquisition



$\Rightarrow n^* = 0$

\downarrow
 $c3^* = 3,1 \text{ ms}$

$c3 = 5,85 \text{ ms}$

$\Rightarrow n = 1$

Notes: The results are consistent with bigger computational load for the MLCL RT processor!

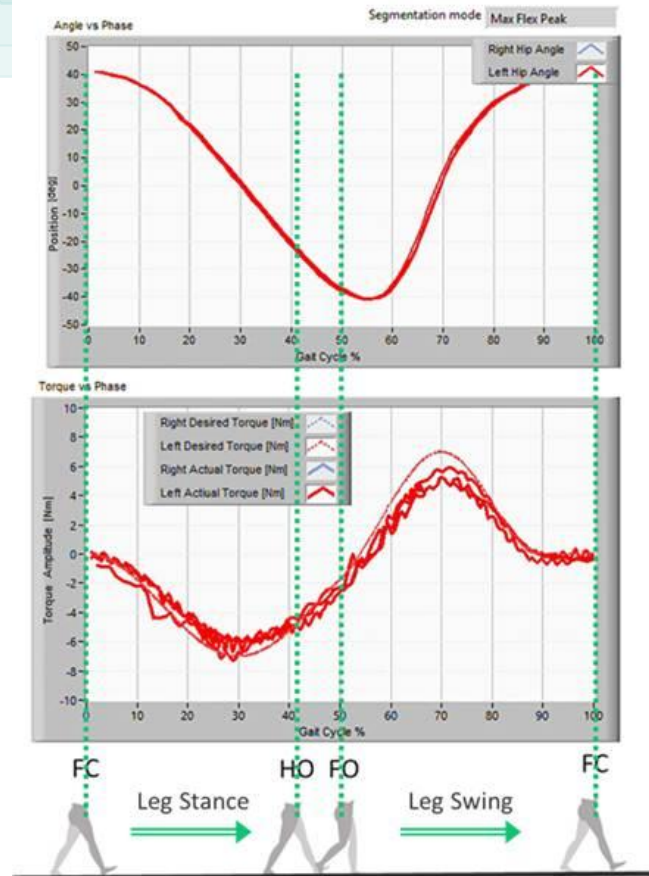
Exercise: Network Differences while Walking

Evaluate the robot performance while providing walking assistance with different network architectures (NA):

- ☐ Fully-integrated
- ☐ Split control architecture, but with the 6GDetCom network delay emulator disabled, and the Debian PC acting as a transparent UDP bridge
- ☐ Split control architecture, with the 6GDetCom network delay emulator enabled*

On the test bench, for each NA:

- ☐ Two different walking velocities (2.5 km/h and 5 km/h)
- ☐ Sinusoidal assistance profiles with ± 7 Nm of peak desired torque**
- ☐ A smart algorithm capable of detecting the walking phase and tracking the movement is used in the MLC^[1]



* The parameters selected for the 6GDetCom network delay emulator are the default [limit 1000, reorder true] and the imported delay distribution is the PD-Wireless-5G-1.

** The selected assistance profile is representative of a typical worker (body weight: 75 kg, height: 175 cm) walking across the shop floor while carrying a previously lifted small box



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Exoskeleton Use Case Validation

IUVO, USTUTT, SSSA

28/05/2025



Conclusions

- ❑ This work described the development of an emulator designed to study novel paradigms for off-loading the control of an occupational exoskeleton using a 6G-oriented wireless network.
- ❑ The observed reduced repeatability in tracking the assistance profiles is particularly critical in real-world use cases involving a human in the loop—especially when the movement is non-periodic or when the high-level controller detects a task transition (e.g., from walking to lifting).
- ❑ To achieve the desired assistance specifications and deliver the intended benefits to the user, the described tool can serve as a powerful co-design verification platform for the exoskeleton control layers, algorithms, and communication network.



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